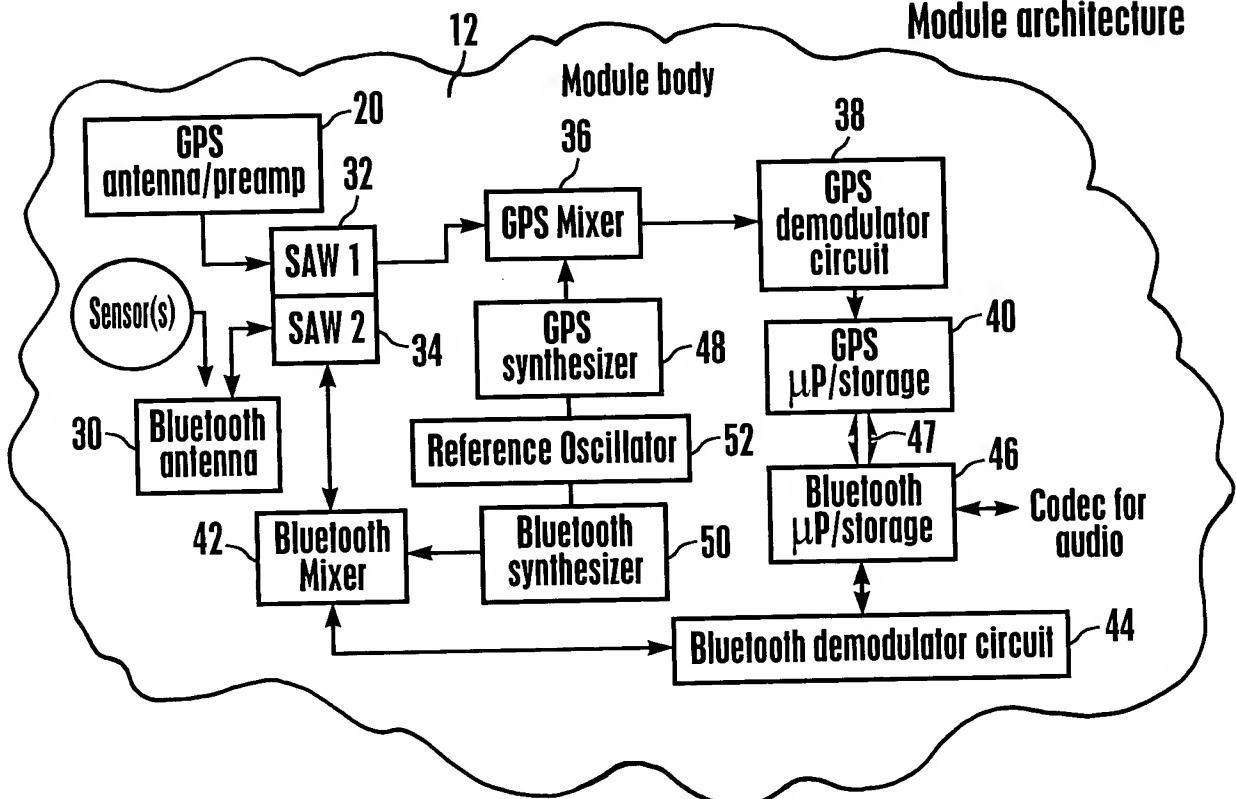


Fig. 1

Fig. 2
Module architecture

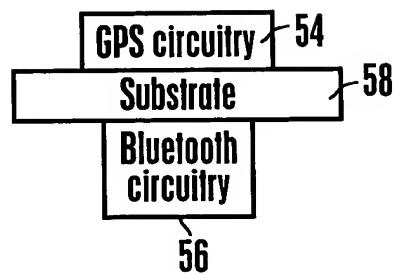


Fig. 3

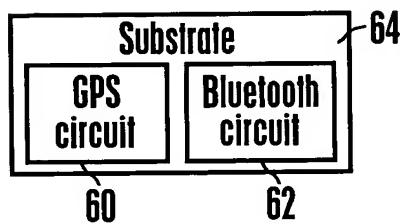


Fig. 4

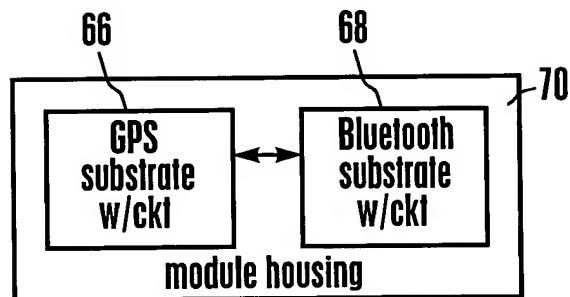


Fig. 5